

A

B

C

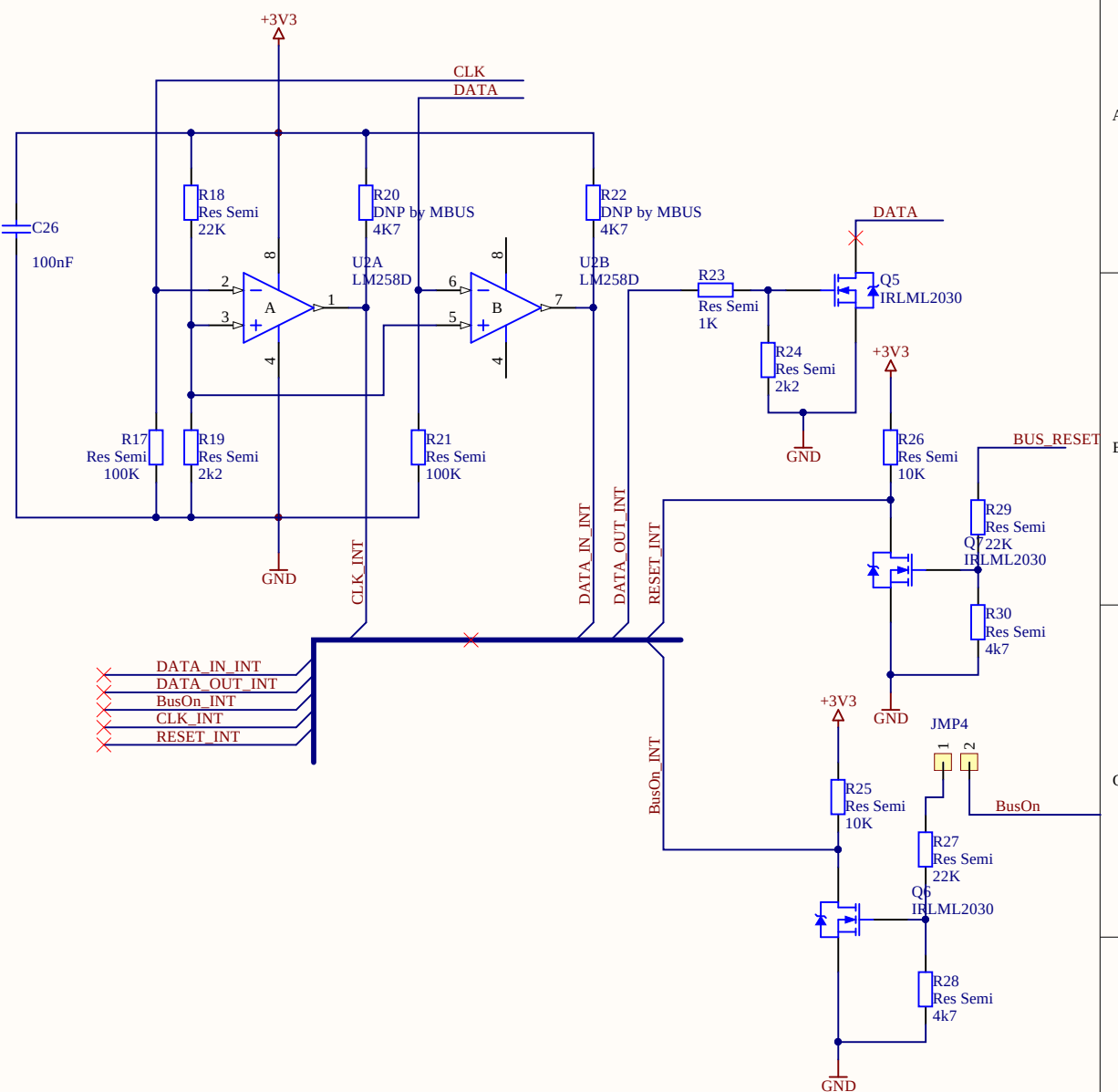
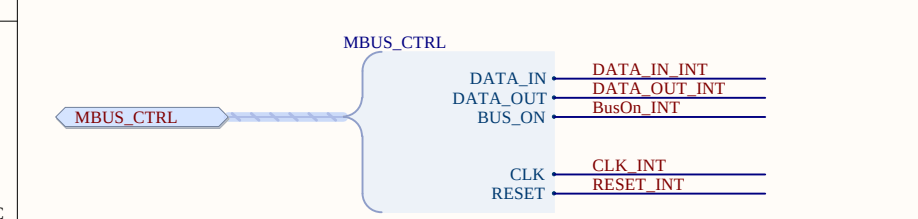
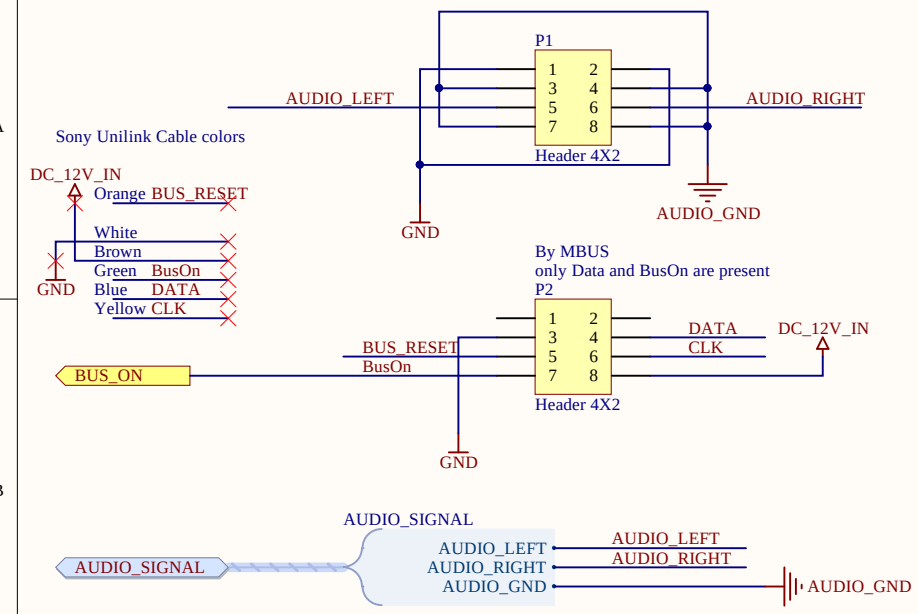
D

A

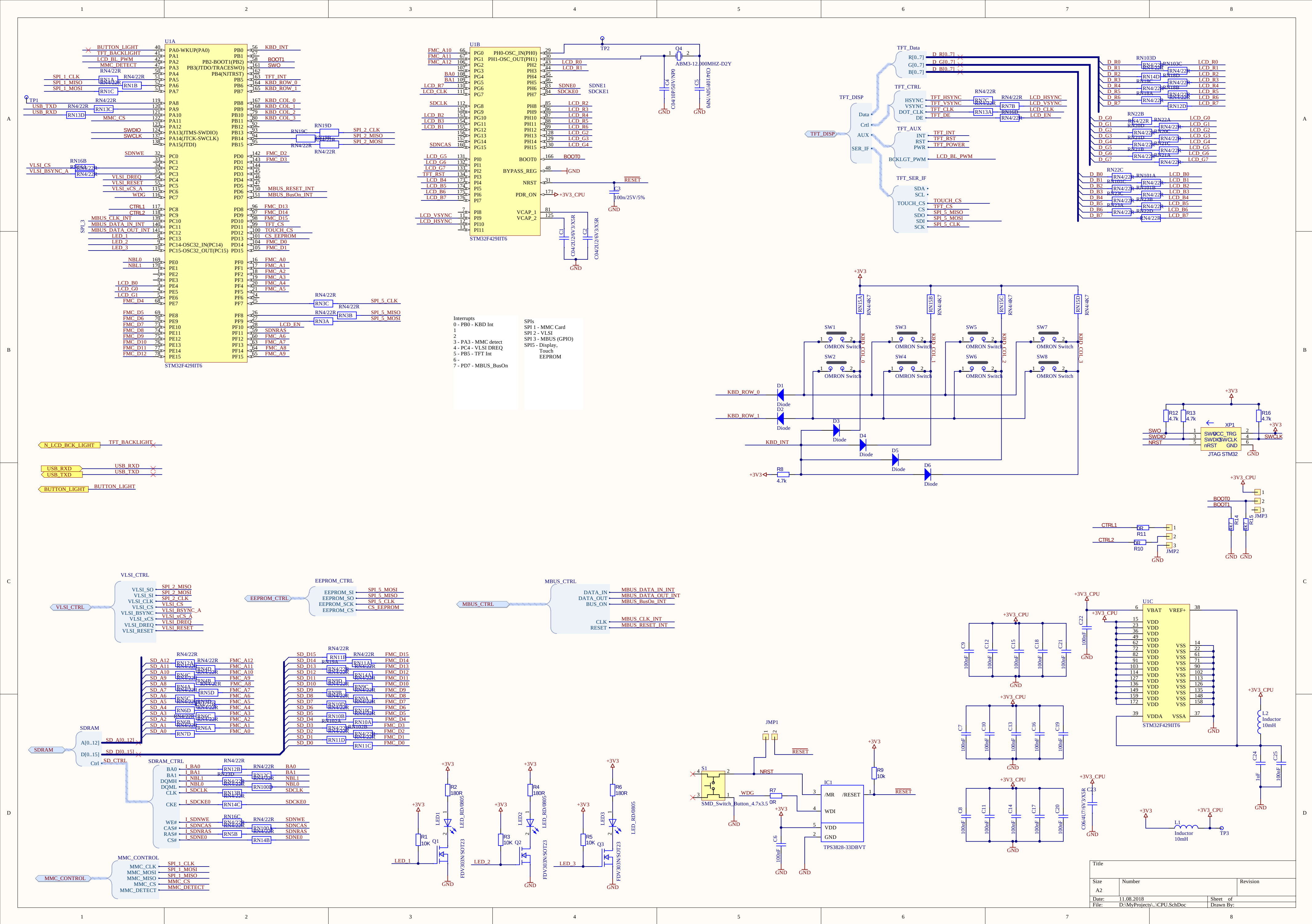
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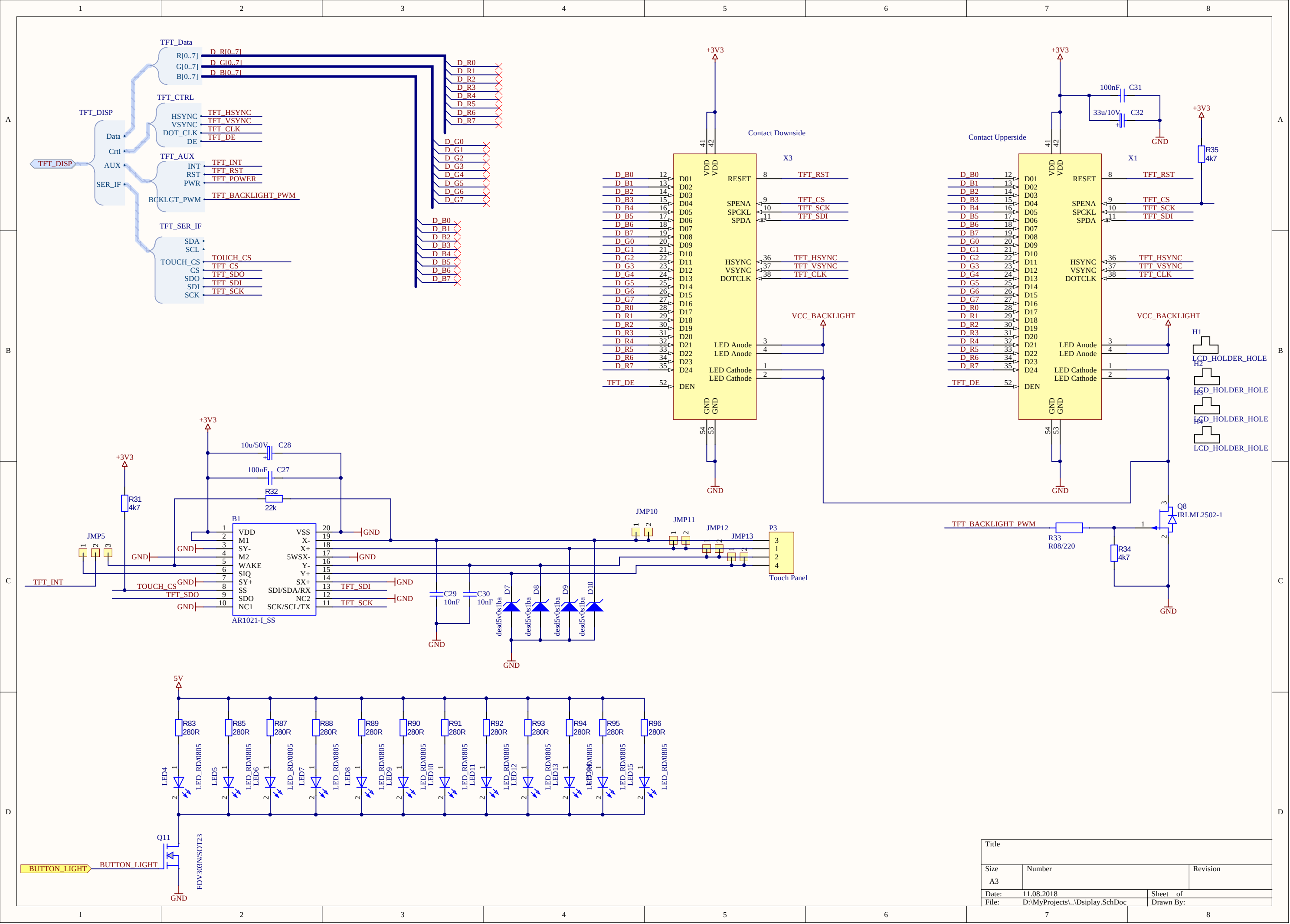
C

D



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Title		
Size	Number	Revision
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Date:	11.08.2018	Sheet of
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A

B

C

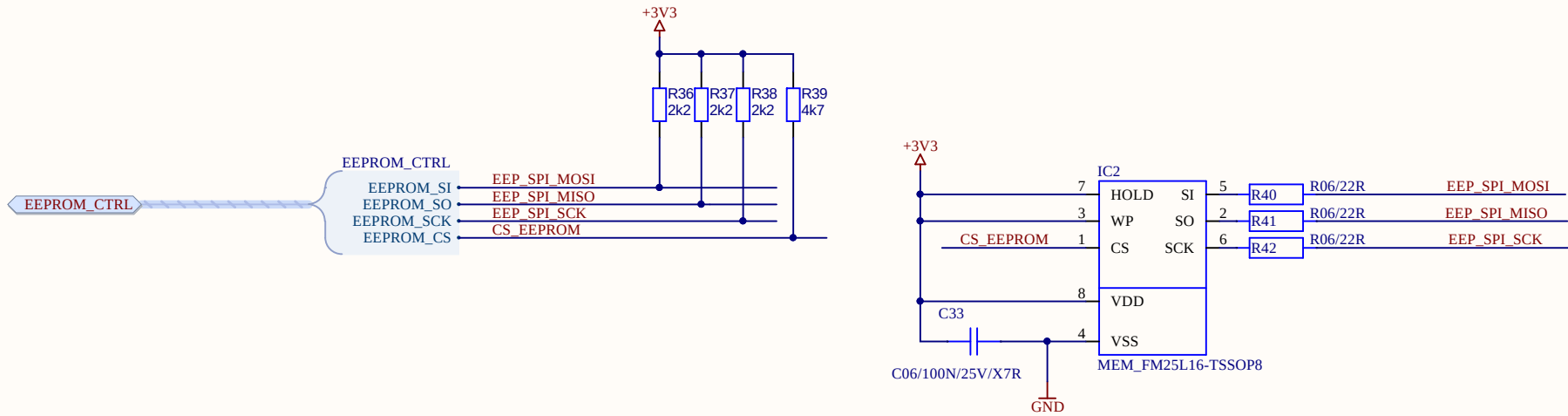
D

A

B

C

D



Title		
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CPU.SchDoc

N_LCD_BCK_LIGHT

SDRAM

TFT_DISP

BUTTON_LIGHT

MMC_CONTROL

VLSI_CTRL

MBUS_CTRL

EEPROM_CTRL

USB_RXD

USB_TXD

U_Power
Power.SchDoc

Bus_ON

N_LCD_BCK_LIGHT

U_SDram
SDram.SchDoc

SDRAM

U_Dsisplay
Dsiplay.SchDoc

TFT_DISP

BUTTON_LIGHT

U_SD_MMC
SD_MMC.SchDoc

MMC_CONTROL

U_VLSI
VLSI.SchDoc

VLSI_CTRL AUDIO_SIGNAL

U_AutoInterface
AutoInterface.SchDoc

MBUS_CTRLAUDIO_SIGNAL

BUS_ON

U_Eeprom
Eeprom.SchDoc

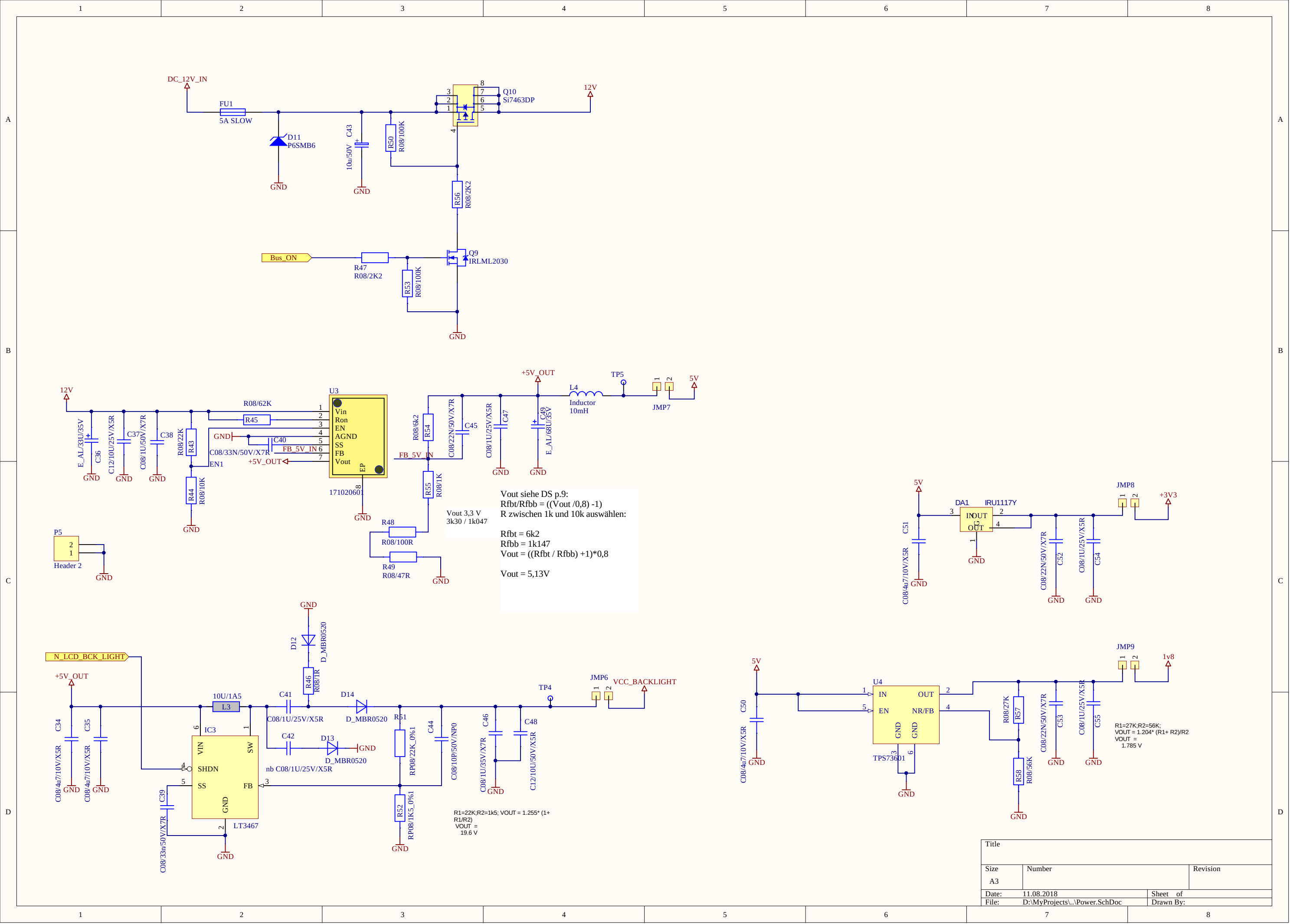
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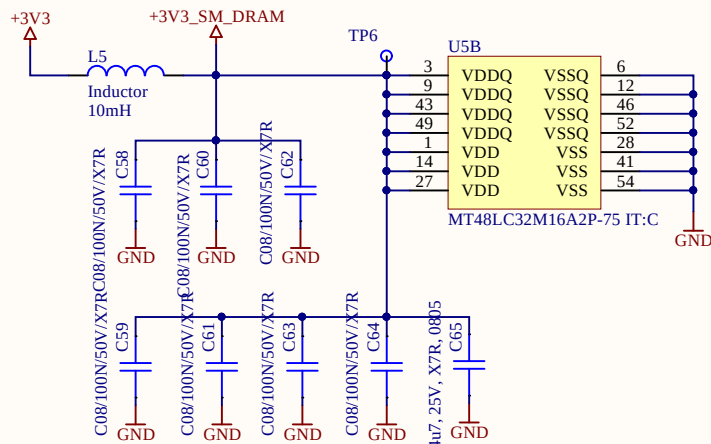
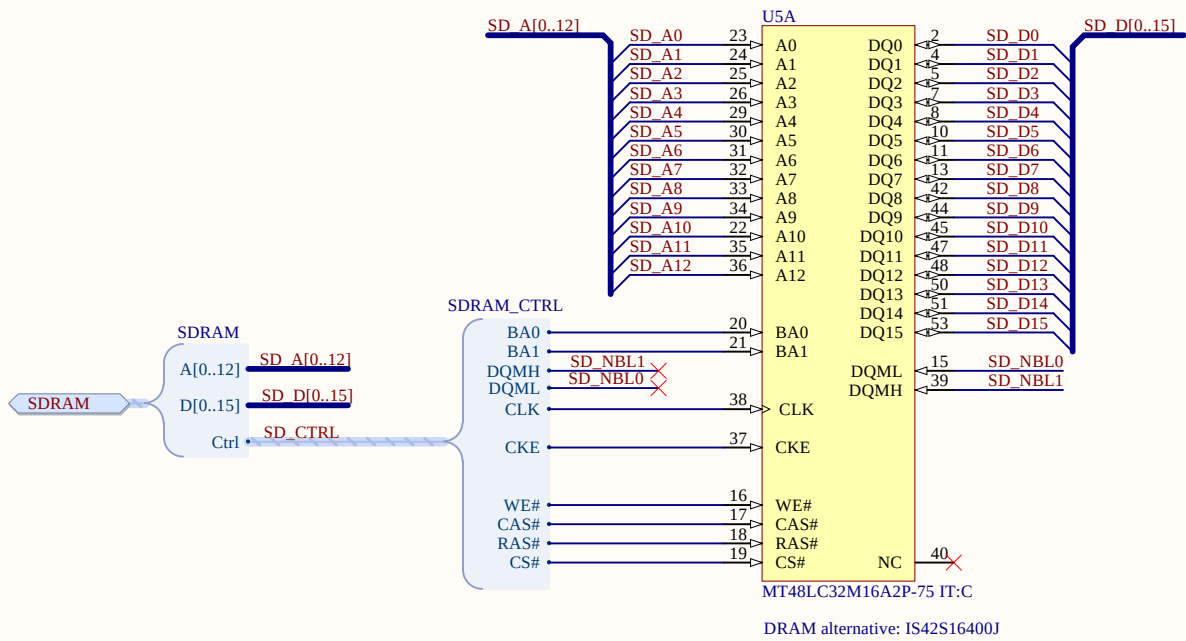
U_USB
USB.SchDoc

USB_RXD

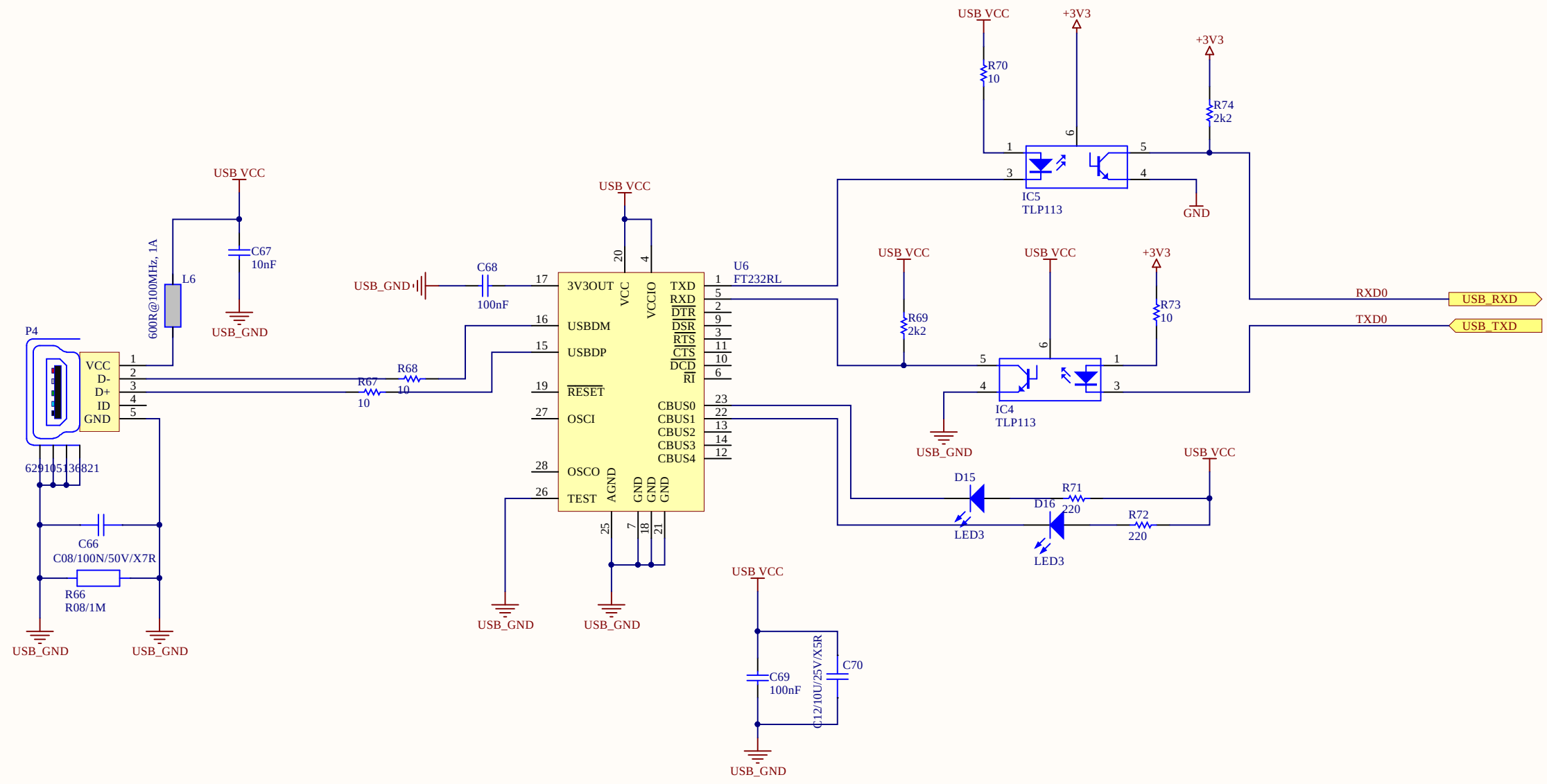
USB_TXD

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Title		
Size	Number	Revision
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Date:	11.08.2018	Sheet of
File:	D:\MyProjects\...\SDram.SchDoc	Drawn By:



Title		
Size	Number	Revision
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Date:	11.08.2018	Sheet of
File:	D:\MyProjects\...\USB.SchDoc	Drawn By:

